

TO COMPLEMENT THE SECURITY VARI-LIGHT



OPTO REPEATER

BY A. R. WINSTANLEY

THE Opto Repeater is designed for use in conjunction with the Security Vari-Light project (last month) and serves to present a more realistic overall illusion to the prospective prowler.

Although the Security Vari-Light will operate a single lamp (or combination of lamps not exceeding 500W), the disadvantage is that only one room can be controlled in this manner.

The Opto Repeater utilises a phototransistor to detect when the main Security Vari-Light is on and, when illuminated by the main lamp, operates a secondary lamp for a preset period.

OPTICAL COUPLING

Thus a system of optical links are used to enable the "master" lamp to control lights in other rooms, so that a larger section of the household appears to be inhabited at night.

The design has been kept simple, the use of complex logic circuitry having been avoided, since the constructor may well wish to assemble more than one unit for use around the home. The low cost of the Opto Repeater will help him to do this.

CIRCUIT DESCRIPTION

Fig. 1 illustrates the circuit diagram for the Opto Repeater. TR1 is the remotely located photo transistor connected via PL1 and SK1 to the trigger input (pin 2) of IC1. This integrated circuit is a 555 timer wired as a simple monostable, the period of which is determined by R2 and C1. This RC network sets the time period to about 8 minutes or so but this may vary in reality, due to quite large manufacturing tolerances on C1.

When TR1 is in darkness, it in effect exhibits a high resistance, such that pin 2 is virtually at the supply potential. However, as light starts to fall upon the photo-transistor the voltage at its collector will fall, bringing down with it the trigger input of IC1 until it triggers.

Thus when the Security Vari-Light illuminates, the position of TR1 will be such that the phototransistor will be illuminated as well, causing IC1 to trigger.

When the i.c. commences timing, its output (pin 3) rises to a little less than the supply rail voltage and this causes a mains-rated relay RLA to activate. Consequently the contacts RLA1 close and complete a circuit between the mains sup-

ply and the remote lamp plugged into SK2.

The timer i.c. will then continue with its timing cycle and under normal circumstances the relay will deactivate at the end of the timing period, thereby extinguishing the remote lamp.

In fact, for *normal* operation to occur it is necessary that the triggering period is less than the timing period set by R2 and C1. This means that the Security Vari-Light must illuminate TR1 for a period less than the timing period of IC1.

If the trigger time exceeds the RC timing period then the i.c. will re-trigger and commence timing again until the trigger signal diminishes, that is, the master lamp extinguishes. The Opto-Repeater's timer will then reset itself also.

MAINS POWER

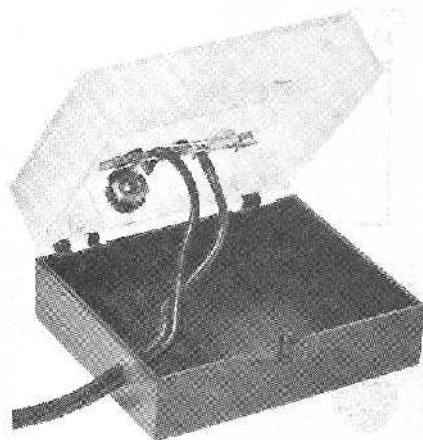
The Opto Repeater is mains powered through T1, a centre-tapped transformer which steps down the mains voltage applied to its primary winding to about 12V a.c. This is full-wave rectified by D3 and D4, and is then smoothed by C4 to produce a d.c. potential of approximately 16V within the maximum rating of the timer i.c.

R3 and C3 form a filter which reduces the ripple present after smoothing, to give an improved supply rail quality.

Mains power is switched by S1. In the SECURITY mode, the Opto Repeater operates as dictated by the light falling upon the phototransistor; in the BYPASS position, power is routed straight through to SK2 via F2 so that the electronic circuit is disconnected and the lamp is continuously illuminated. In the centre OFF position, both lamp and timer are disabled.

However, the presence of an arc suppressor, X1, means that some power may be transmitted through it when S1 is in the BYPASS mode. This means that although the transformer primary may appear to be disconnected, the electronics may in fact be partially operative, but this is nothing to worry about.

Phototransistor mounted in a clear fronted plastic box.



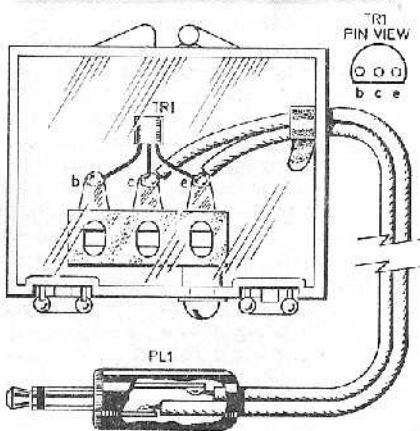
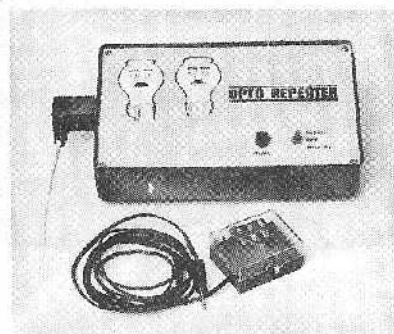
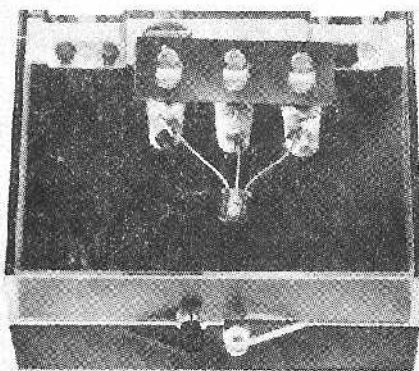
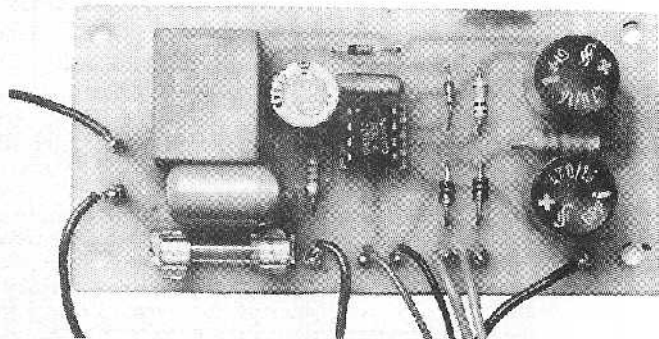


Fig. 4. Assembly of the phototransistor unit. Note that the curved face of TR1 is photosensitive.



Plastic "stylus" box housing tag strip and TR1.



The completed prototype p.c.b. assembly. Note the use of an i.c. holder for IC1 and Veropins for all wired connections.

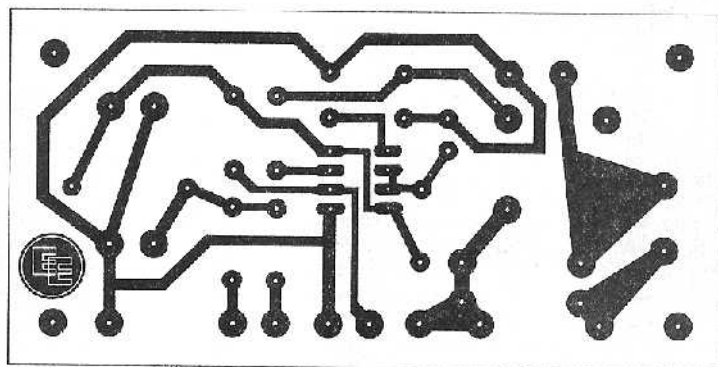
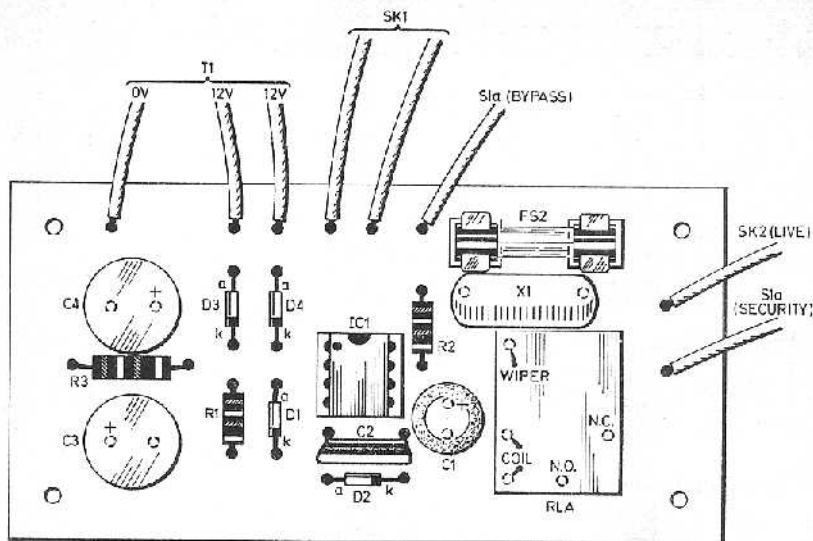
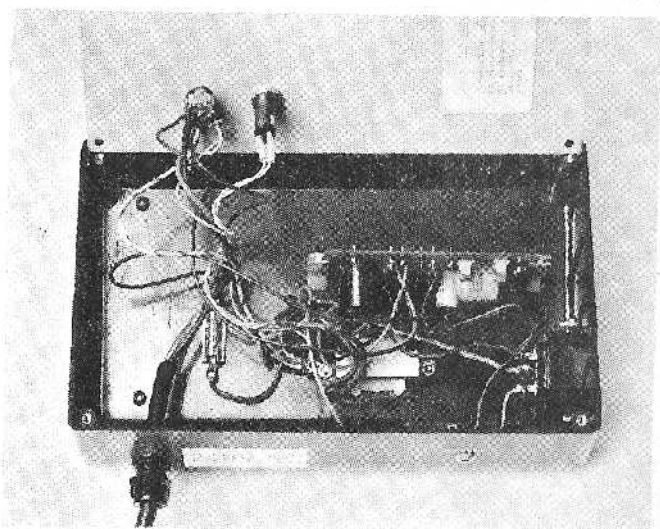


Fig. 2. Full size track pattern and component layout for the Opto Repeater circuit board. Note that the board is designed to accept the Maplin "ultra miniature high power mains relay".



View inside the console case showing the relative positions of the main components.

